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Washington, D.C. 20231

On August 7, 2001

TOWNSEND and TOWNSEND and CREW LLP

By: Julia Taylor Clough



#4 Attorney Docket: PATENT
20181-51

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Boris A. Babaian, et al.

Application No.: 09/838,550

Filed: April 18, 2001

For: METHOD FOR EFFECTIVE
BINARY TRANSLATION BETWEEN
DIFFERENT INSTRUCTION SETS
USING EMULATED SUPERVISOR
FLAG AND MULTIPLE PAGE TABLES

Examiner:

Art Unit: 2181

INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The references cited on attached form PTO-1449 are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

Applicant believes that no fee is required for submission of this statement, since it is being submitted prior to the first Office Action. However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



Charles J. Kulas
Reg. No. 35,809

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

2

Complete if Known

Application Number

09/838,550

Filing Date

April 18, 2001

First Named Inventor

Babaian, Boris A.

Group Art Unit

2181

Examiner Name

Attorney Docket Number

020181-005100US

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

**Examiner
Signature**

Date
Considered

* **EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08B (08-00)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	09/838,550
Filing Date	April 18, 2001
First Named Inventor	Babaian, Boris A.
Group Art Unit	2181
Examiner Name	
Attorney Docket Number	020181-00510US

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AD	SITES, RICHARD L., et al., "Binary Translation," Communications of the ACM, February 1993, Vol. 36, No. 2, pages 69-81.	<input type="checkbox"/>
	AE	DOBBERPUHL, DANIEL, et al., "Design of EV-4," Communications of the ACM, February 1993, Vol. 36, No. 2, page 82. (one-page - stapled to back of Cite AD above)	<input type="checkbox"/>
	AF	DIEFENDORFF, KEITH, "The Russians are Coming - Supercomputer Maker Elbrus Seeks to Join x86/1A-64 Melee," Microprocessor Report, February 15, 1999, Vol. 13, No. 2, pages 1-7.	<input type="checkbox"/>
	AG	SILBERMAN, GABRIEL M., et al., "An Architectural Framework for Supporting Heterogeneous Instruction-Set Architectures," Computer, June 1993, Vol. 26, No. 6, pages 39-56.	<input type="checkbox"/>
	AH	RIVEST, RONALD L., "The MD5 Message-Digest Algorithm," Memo, Network Working Group, MIT Laboratory for Computer Science and RSA Data Security, Inc., April 1992, 21 pages.	<input type="checkbox"/>
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